

FIG. 1

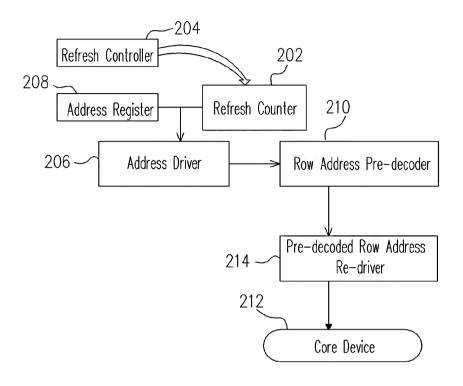


FIG. 2

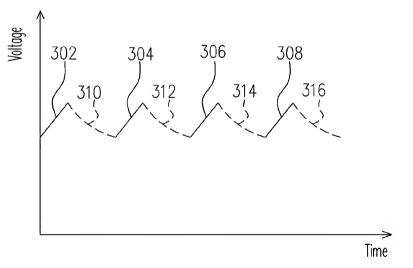


FIG. 3

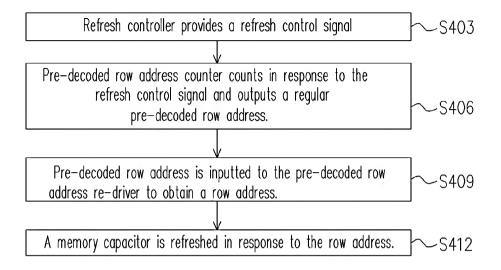


FIG. 4 -504 Refresh Controller 508 -512 -520 522 -524 -526 Pre-decoded Row Address Pre-decoded Row Address -528 Counter Re-driver -530 **-532** 516 Core Device

FIG. 5

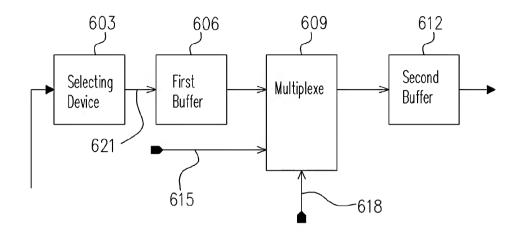


FIG. 6

